	Application No.	Applicant(s)	
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Nation of Allowability	10/612,263	RINERSON ET AL.	
Notice of Allowability	Examiner	Art Unit	
	Tuan T. Nguyen	2824	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS Is herewith (or previously mailed), a Notice of Allowance (PTOL-8: NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.3	S (OR REMAINS) CLOSED in 5) or other appropriate communication is selection is selection in the selection i	n this application. If not included unication will be mailed in due co	ł ourse. THIS
1. This communication is responsive to <u>amendment filed on</u>	<u> 5/5/05</u> .		
2. The allowed claim(s) is/are <u>1-26</u> .		•	
3. The drawings filed on <u>01 July 2003</u> are accepted by the E	Examiner.		
4. Acknowledgment is made of a claim for foreign priority of a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority of International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ve been received. ve been received in Application ocuments have been received " of this communication to file	on No d in this national stage application	٠.
5. A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which gi			TICE OF
 6. CORRECTED DRAWINGS (as "replacement sheets") mutual control of the Notice of Draftspe 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examine Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in 	rson's Patent Drawing Reviev r's Amendment / Comment or 1.84(c)) should be written on the	in the Office action of ne drawings in the front (not the b	eack) of
 DEPOSIT OF and/or INFORMATION about the dep attached Examiner's comment regarding REQUIREMENT 			ote the
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview St Paper No./ /08), 7. ☑ Examiner's	formal Patent Application (PTO- ummary (PTO-413), Mail Date Amendment/Comment Statement of Reasons for Allow te search hitory.	

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DETAILED ACTION

1. This is the response to the Applicant's Amendment filed on 5/5/05.

Examiner's Amendment

2. Claim 16, line 1, change 14 to 15.

Allowable Subject Matter

3. Claim1-26 are allowed.

4. The following is an examiner's statement of reasons for allowance:

The prior art of record fail to disclose a driver set for a cross point memory array comprising, in combination with other cited limitations, a plurality of first x-direction lines on a second fabrication layer above the active surface of the semiconductor substrate that electrically connects the transistor of a line driver group to each other; a plurality of second x-direction lines on the second fabrication layer each second x-direction line electrically connecting the line drivers in each line driver group; a plurality of third x-direction lines on the second fabrication layer, each third x-direction line electrically connected to a single line driver as recited in claims 1-9.

The prior art of record also fail to disclose a driver set for a cross point memory array comprising, in combination with other cited limitation, a plurality of line driver groups that are in electrical communication with a nonvolatile two-terminal cross point memory array wherein the electrical communication is achieved in two metallization layers as recited in claims 10-11.

The prior art of record fail to disclose a driver set comprising, in combination with other cited limitations, a first metal layer that electrically connects all the transistors in the same relative position to each other with conductive lines in the first direction and provides an

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extension for one line driver stage in a second direction, the second direction being orthogonal to the first direction; and a second metal layer that electrically connects all the transistors to a memory array, and electrically connects all the line driver stages in each line driver group, whereby the electrical connection in the one line driver stage is made via the extension as recited in claim 12.

The prior art of record further fail to disclose a re-writable cross point memory array comprising, in combination with other cited limitations, one or two x-direction conductive layers that include conductive array lines, the conductive array lines electrically connected to the x direction line driver groups; and one y-direction conductive layer mat include conductive array lines, the conductive array lines electrically connected to the y direction line driver groups as recited in claims 13-14.

The prior art of record also fail to disclose a re-writable cross point memory array comprising, in combination with other cited limitations, three x-direction conductive layer that include conductive array lines, two y-direction conductive layer that include conductive array lines as recited in claims 15-16.

The prior art further fail to disclose a cross point memory array comprising, in combination with other cited limitations, a plurality of x-direction line drives that drive x-direction conductive array lines; wherein at least some x-direction line drivers connect with x-direction conductive lines at a location other than the line ends as recited in claims 17-18.

The prior art of record also fail to disclose a cross point memory array comprising, in combination with other cited limitations, wherein the plurality of x-direction conductive array lines includes an array cut that is where two contiguous x-direction conductive array lines a

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spaced further apart than other contiguous x-direction conductive array lines as recited in claim 19-26.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Nguyen whose telephone number is (571) 272-1880. The examiner can normally be reached on Mon-Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

July 11, 2005

Tuan T. Nguyen Primary Examiner Art Unit 2824

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